Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	832	327/530-550.ccls. and (bias adj2 circuit)	USPAT	OR	ON	2005/03/01 16:33
L3	385	1 and ((transmission adj gate) CMOS)	USPAT	OR	ON	2005/03/01 16:21
L4	113	3 and (high adj2 impedance)	USPAT	OR	ON	2005/03/01 16:21
L5	34	327/530-550.ccls. and ((bias adj2 circuit) same (high adj impedance))	USPAT	OR	ON	2005/03/01 16:34
L6	45	327/530-550.ccls. and ((bias adj2 circuit) same (high adj impedance))	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/03/01 16:39
L8	17	327/530-550.ccls. and ((bias adj2 circuit) and (((high adj2 impedance) near3 node)))	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/03/01 16:40

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	9	("6538466" "6262593" "6211704" "5834948" "5748025" "5726588" "4956691" "6424193" "5831451").pn.	USPAT	OR	OFF	2005/02/24 15:49
S2	1	(("6538466" "6262593" "6211704" "5834948" "5748025" "5726588" "4956691" "6424193" "5831451").pn.) and leakage	USPAT	OR	OFF	2004/09/15 16:43
S3	5949	(MOSFET) and (leakage adj current)	US-PGPUB; USPAT	OR	ON	2004/09/15 16:45
S4	1127	(MOSFET) and ((leakage adj current) near3 (reduce lower eliminate))	US-PGPUB; USPAT	OR	ON	2004/09/15 16:45
S5	172	(MOSFET) and (((leakage adj current) near3 (reduce lower eliminate)) same bias)	US-PGPUB; USPAT	OR	ON	2004/09/15 16:46
S6	117	((MOSFET) and (((leakage adj current) near3 (reduce lower eliminate)) same bias)) and switch	US-PGPUB; USPAT	OR	ON	2004/09/16 08:57
S7	1	"5917365".pn.	US-PGPUB; USPAT	OR	ON	2004/09/16 08:58
S8	1414	327/147,148,156,157.ccls. and ((bias circuit) or (bias voltage))	USPAT	OR	OFF	2004/09/16 10:33
S9	6	327/147,148,156,157.ccls. and (((bias circuit) or (bias voltage)) and ((leakage adj current) near3 (reduce lower eliminate)))	USPAT	OR	OFF	2004/09/16 11:00
S10	6	327/147,148,156,157.ccls. and (((bias adj circuit) or (bias adj voltage)) and ((leakage adj current) near3 (reduce lower eliminate)))	USPAT	OR	ON	2004/09/16 11:09
S11	0	324/76.39,76.52,76.53 and (((bias adj circuit) or (bias adj voltage)) and ((leakage adj current) near3 (reduce lower eliminate)))	USPAT	OR	ON	2004/09/16 11:13
S12	0	324/76.39,76.52,76.53.ccls. and (((bias adj circuit) or (bias adj voltage)) and ((leakage adj current) near3 (reduce lower eliminate)))	USPAT	OR	ON	2004/09/16 11:13
S13	0	324/76.39,76.52,76.53.ccls. and (((leakage adj current) near3 (reduce lower eliminate)))	USPAT	OR	ON	2004/09/16 11:14
S14	6	324/76.39,76.52,76.53.ccls. and (leakage adj current)	USPAT	OR	ON	2004/09/16 11:41
S15	75	327/436.ccls.	USPAT	OR	ON	2004/09/16 11:46

S16	1	327/436.ccls. and ((leakage adj current) near3 (reduce lower eliminate))	USPAT	OR	ON	2004/09/16 11:45
S17	19	327/436.ccls. and ((bias adj circuit) or (bias adj voltage))	USPAT	OR	ON	2004/09/16 11:56
S18	0	327/427-437.ccls. and 324/76.53.ccls.	USPAT	OR	ON	2004/09/16 11:57
S19	1	327/427-437.ccls. and 324/76.39.ccls.	USPAT	OR	ON	2004/09/16 12:15
S20	14	327/427,433,434,436,437.ccls. and ((leakage adj current) near3 (reduce lower eliminate))	USPAT	OR	ON	2004/09/16 12:34
S21	17	327/427,433,434,436,437.ccls. and ((leakage adj current) near3 (reduce reducing lower eliminate))	USPAT	OR	ON	2004/09/16 13:23
S22	3	(327/427,433,434,436,437.ccls. and ((leakage adj current) near3 (reduce reducing lower eliminate))) not (327/427,433, 434,436,437.ccls. and ((leakage adj current) near3 (reduce lower eliminate)))	USPAT	OR	ON	2004/09/16 13:23
S23	1	"6605935".pn.	USPAT	OR	OFF	2004/09/16 17:09
S24	3	("5583821" "5917365" "5892377").pn.	USPAT	OR	OFF	2004/09/16 17:10
S25	9	"324"/$.ccls. and (((phase adj lock) (phased adj locked)) same ((bias adj circuit) (bias adj voltage) (leakage adj current)))	US-PGPUB; USPAT; USOCR; EPO	OR	OFF	2005/02/24 16:29
S26	42	"327"/$.ccls. and (((phase adj lock) (phased adj locked)) same ((bias adj circuit) (bias adj voltage) (leakage adj current)))	US-PGPUB; USPAT; USOCR; EPO	OR	OFF	2005/02/24 16:30
S27	4	((bias adj circuit) with ((reduc$3 decreas$ lower$3) near3 (leakage adj2 current))) same (transistor CMOS (transistor adj2 gate))	US-PGPUB; USPAT; USOCR; EPO	OR	OFF	2005/02/25 08:51
S28	143	((plurality many multiple pluaral three triple) near3 (CMOS (transmission adj gate))) and (bias adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/25 08:53
S29	40	S28 and (high near3 impedance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/25 09:56

S30	22	326/64-66.ccls. and (bias adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/25 09:59